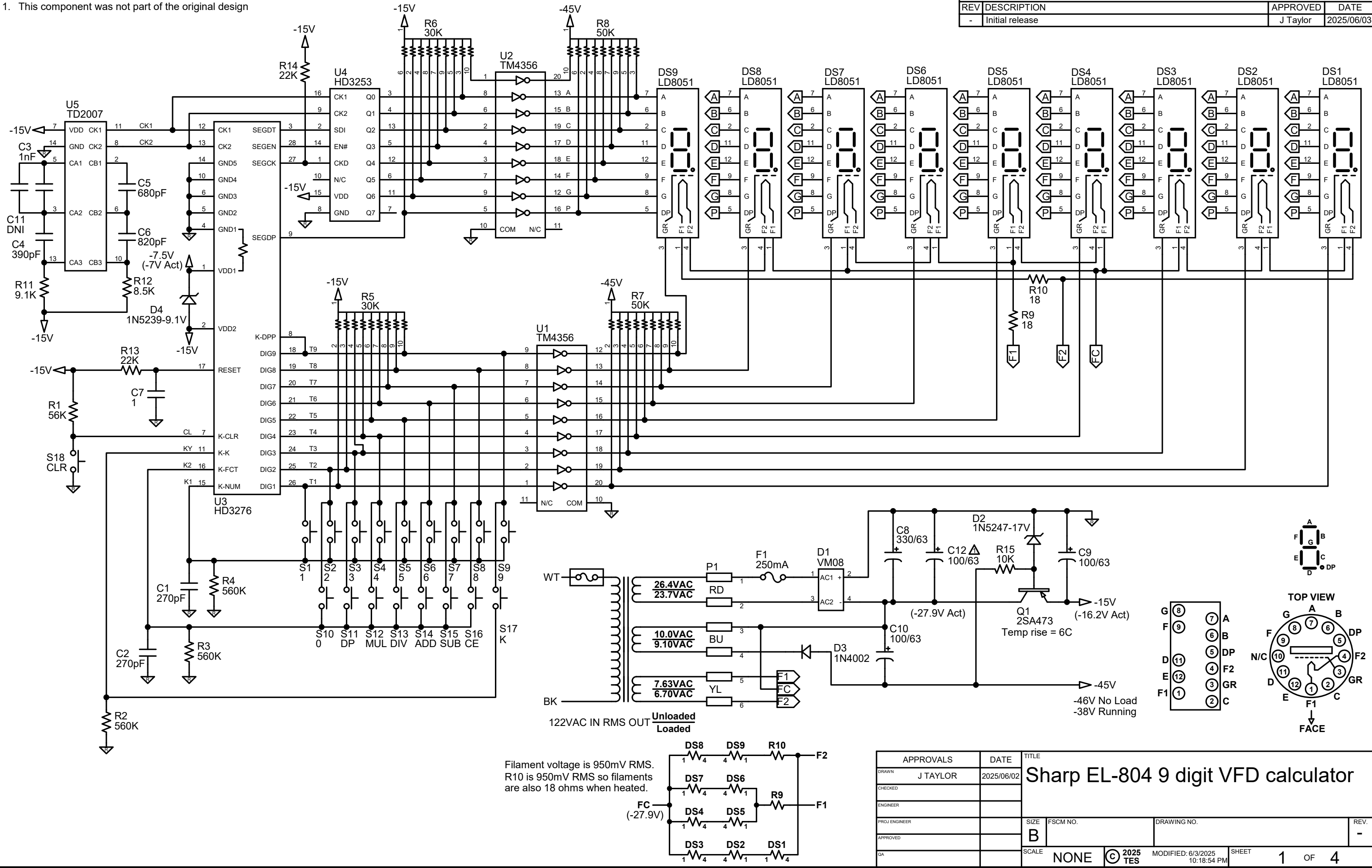
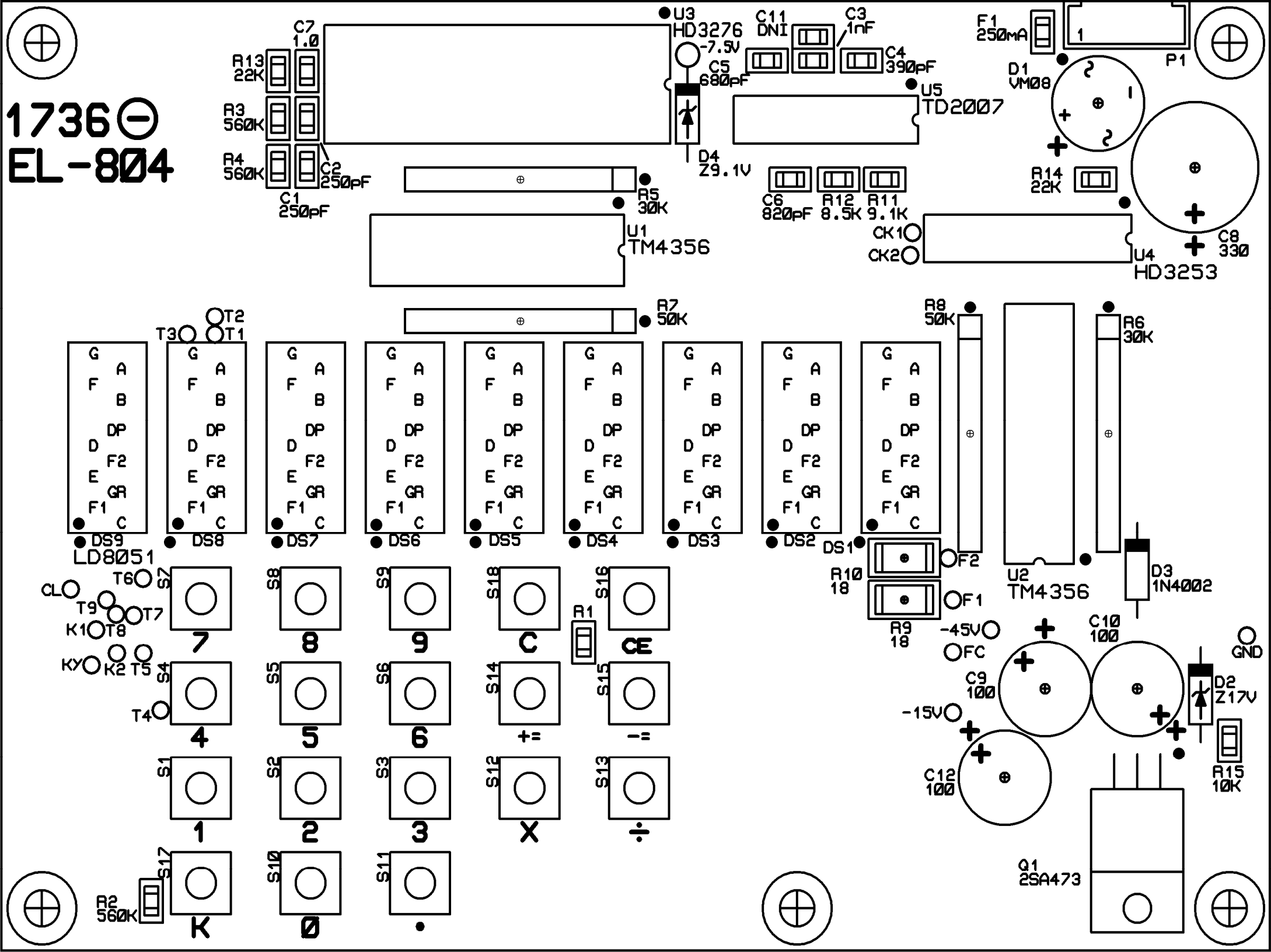


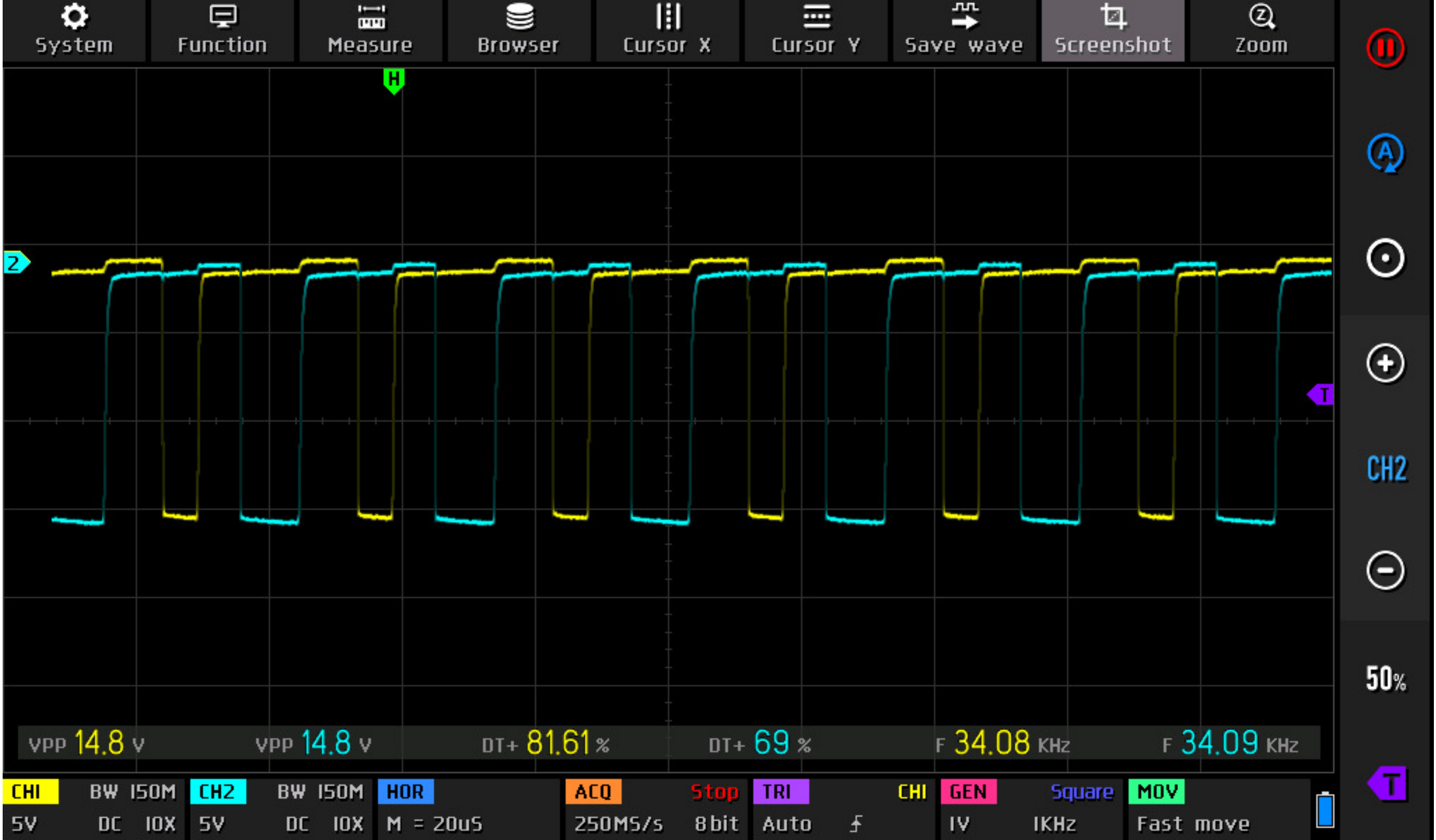
NOTES: UNLESS OTHERWISE SPECIFIED
1. This component was not part of the original design

REVISION HISTORY			
REV	DESCRIPTION	APPROVED	DATE
-	Initial release	J Taylor	2025/06/03



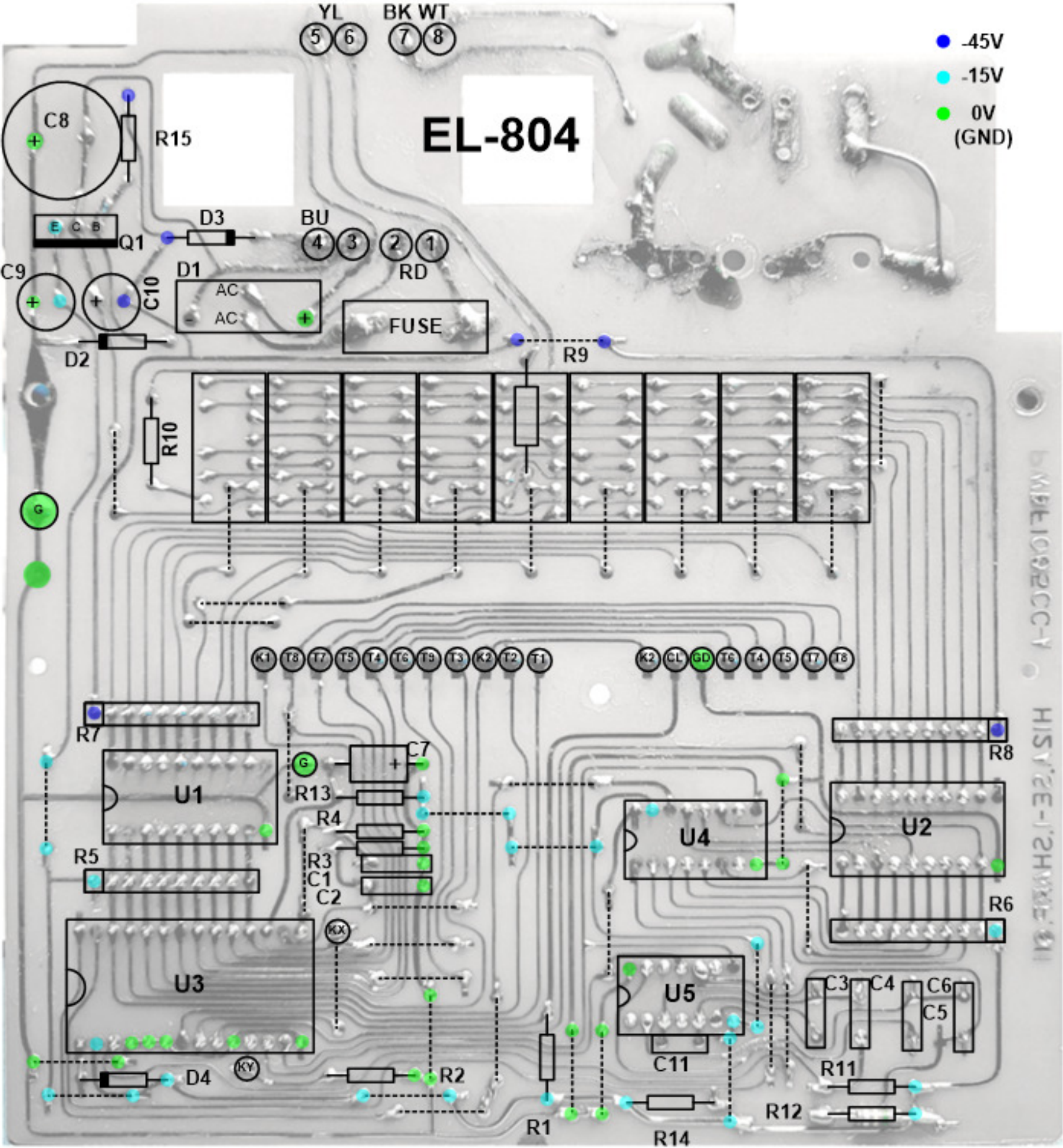


APPROVALS		DATE	TITLE			
DRAWN	J TAYLOR	2025/06/02	Assembly drawing			
CHECKED						
ENGINEER						
PROJ ENGINEER						
APPROVED			SIZE	FSCM NO.	DRAWING NO.	REV.
QA			B			-
SCALE			NONE	© 2025 TES	MODIFIED: 6/3/2025 10:18:54 PM	SHEET 2 OF 4



Channel1 = CK1 / Channel2 = CK2

APPROVALS		DATE	TITLE			
DRAWN	J TAYLOR	2025/06/02	2 Phase clock waveform			
CHECKED						
ENGINEER						
PROJ ENGINEER						
APPROVED			SIZE	FSCM NO.	DRAWING NO.	REV.
QA			B			-
			SCALE	NONE	© 2025 TES	MODIFIED: 6/3/2025 10:18:54 PM
					SHEET	3 OF 4



APPROVALS		DATE	TITLE		
DRAWN	J TAYLOR	2025/06/02	Original calculator PWB Copper shown is actually bottom side		
CHECKED					
ENGINEER					
PROJ ENGINEER			SIZE	FSCM NO.	DRAWING NO.
APPROVED			B		-
QA			SCALE	NONE	© 2025 TES MODIFIED: 6/3/2025 10:18:54 PM
			SHEET	4	OF 4