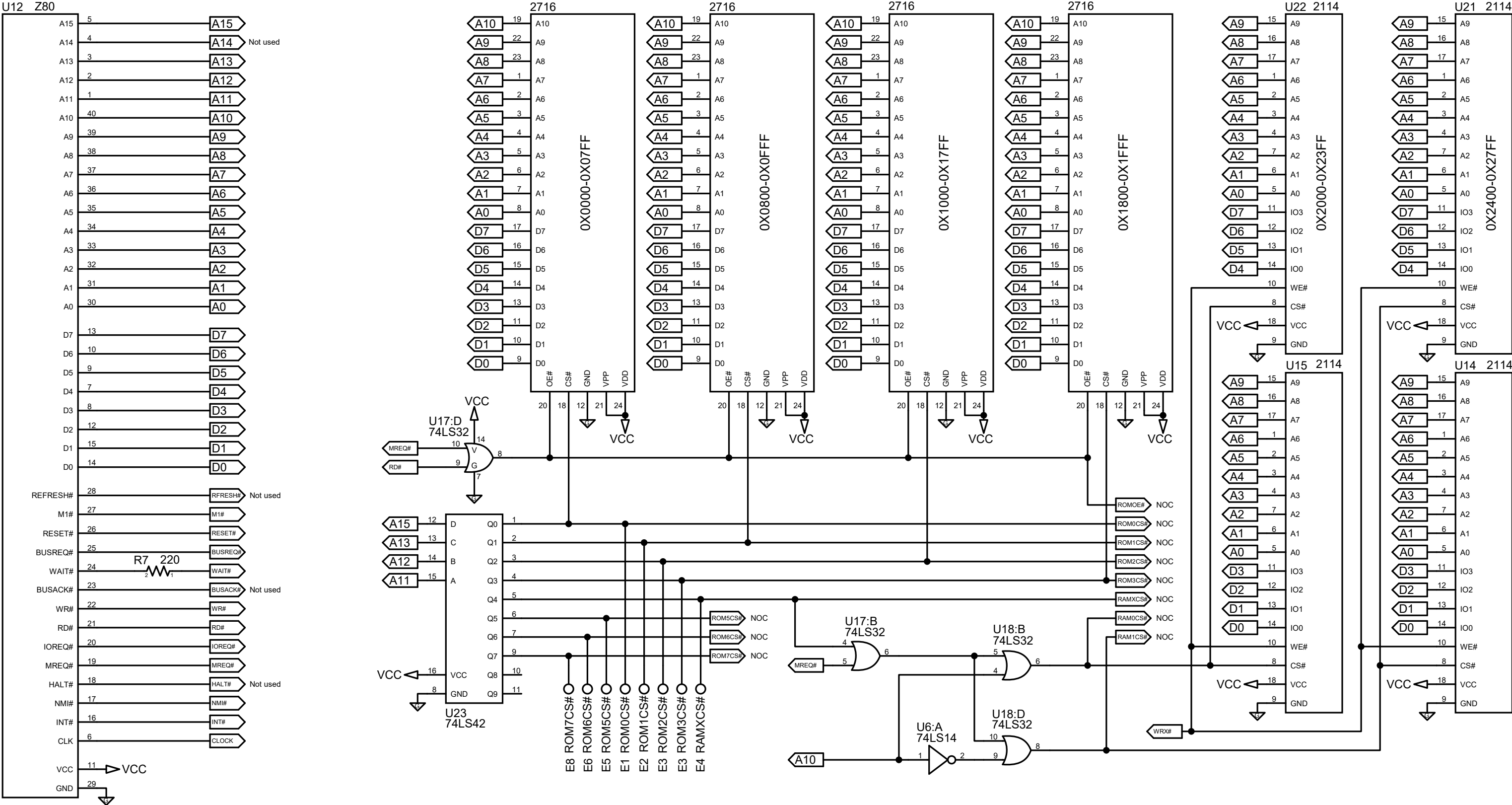


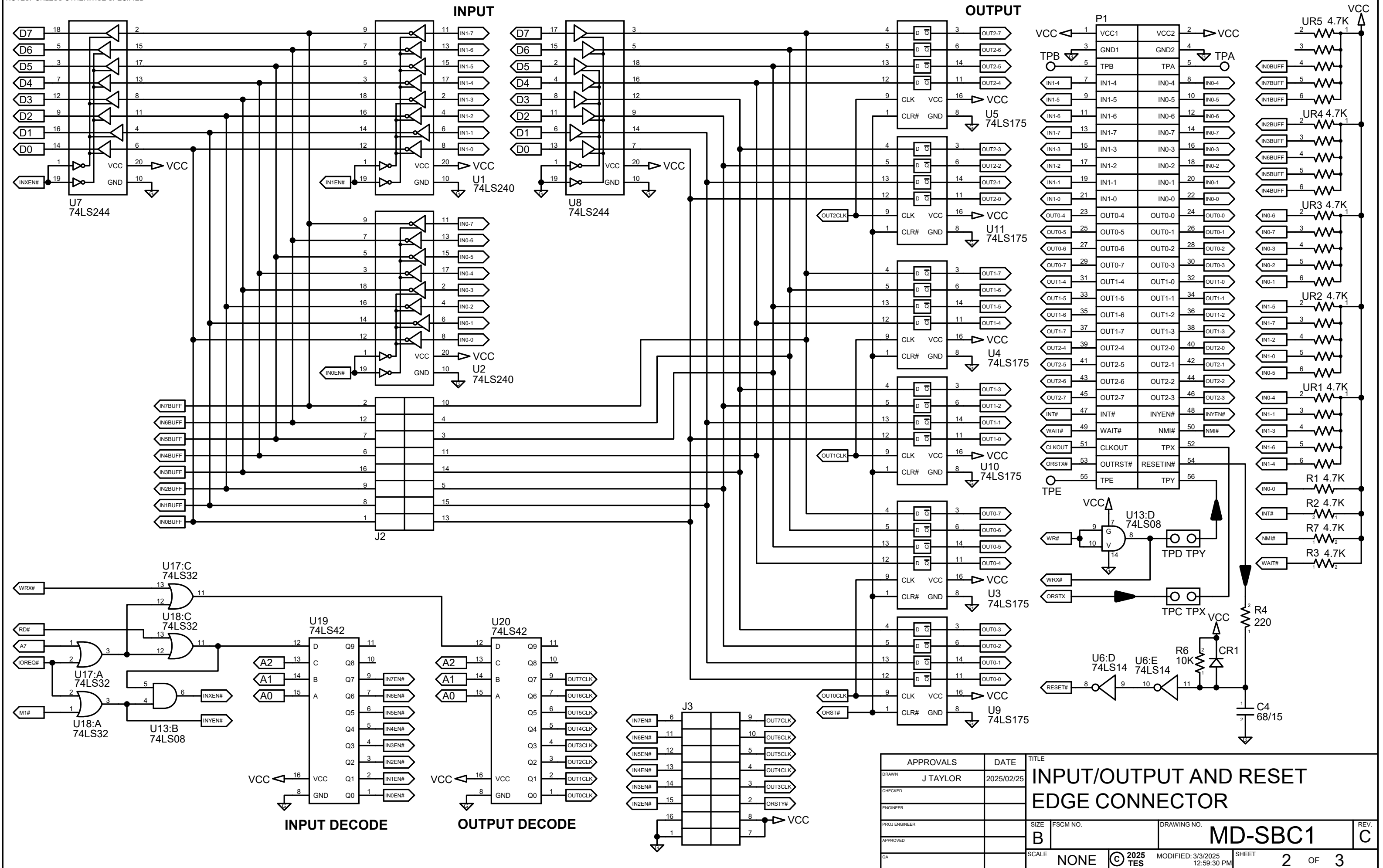
NOTES: UNLESS OTHERWISE SPECIFIED

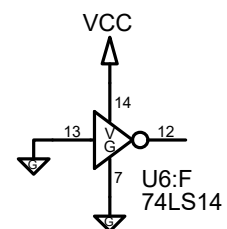
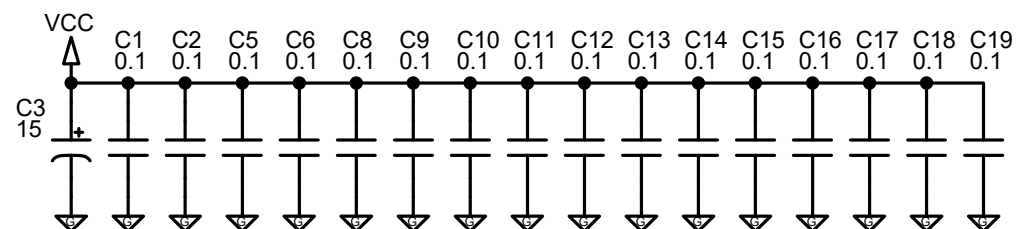
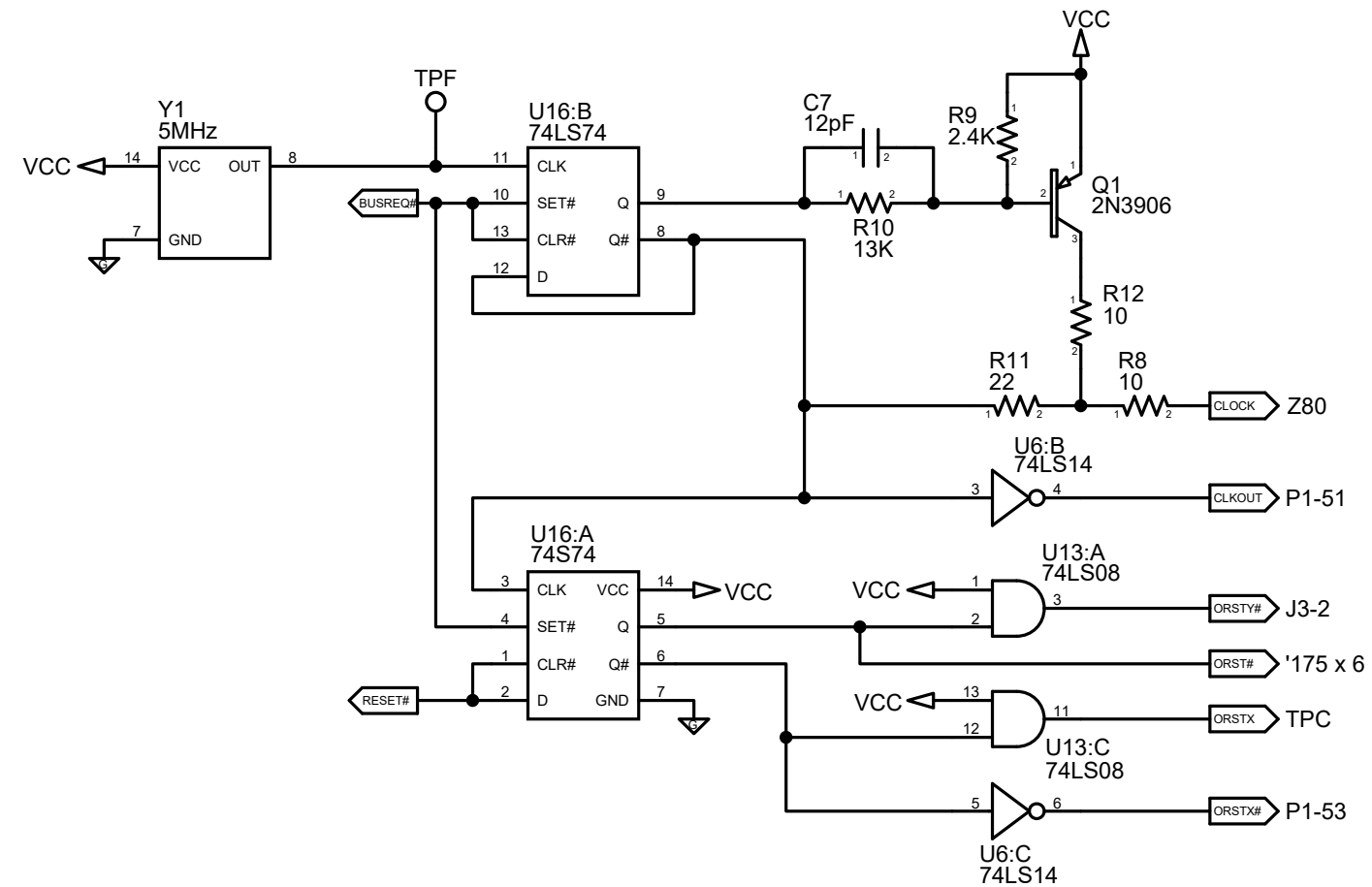
REVISION HISTORY			
REV	DESCRIPTION	APPROVED	DATE
B	Initial release	J Taylor	2025/02/25



APPROVALS		DATE	TITLE		
DRAWN	J TAYLOR	2025/02/25	CPU / ROM AND RAM DECODE		
CHECKED					
ENGINEER					
PROJ ENGINEER			SIZE	FSCM NO.	DRAWING NO.
APPROVED			B		MD-SBC1
QA			SCALE	NONE	REV. C
			© 2025 TES	MODIFIED: 3/3/2025 12:59:30 PM	SHEET 1 OF 3

NOTES: UNLESS OTHERWISE SPECIFIED





APPROVALS		DATE		TITLE CLOCK GENERATION SPARES AND DECOUPLING			
DRAWN J TAYLOR		2025/02/25					
CHECKED							
ENGINEER							
PROJ ENGINEER							
APPROVED				SIZE B	FSCM NO.	DRAWING NO. MD-SBC1	REV. C
QA				SCALE NONE	 2025 TES	MODIFIED: 3/3/2025 12:59:30 PM	SHEET 3 OF 3